Fig. 1 D/A DSP FILTER (10, 24KHz) A/D 7 456.5KHz 456. 5KHz 10.2450 ~10.2455MHZ 455KHz 8 RAM 73 10.7015MHz CIRCUIT 10: 7MHz 6 © 59, 299 ~59, 300MHZ ROM INTERFACE 70.0015MHz 70.0010MHz 21b~ CPU 80.0010 ~80.0015MHZ 70MHz 21a 另下 10MHz

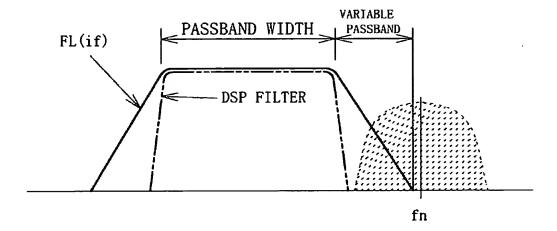


Fig. 3

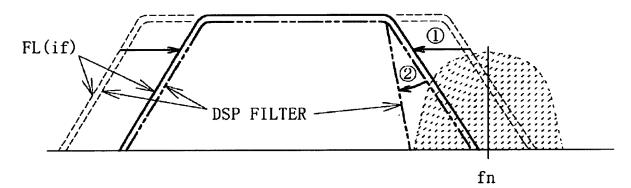


Fig. 4

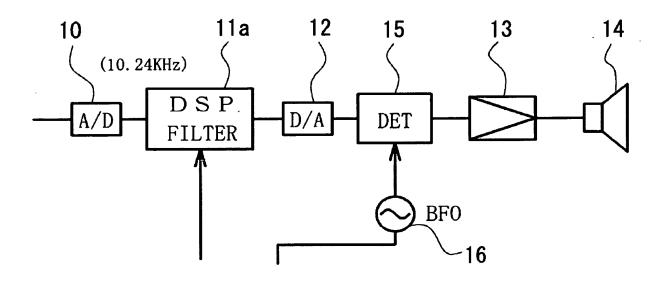


Fig. 5

